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Patent

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Anderson, et al.

Serial No. 09/007,019

Filed: January 14, 1998

For: EXECUTION CONTROL FOR
PROCESSOR TASKS

) Examiner: Lao, Sue X.

) Art Unit: 2126

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AMENDMENT AND RESPONSE TO OFFICE ACTION

In the Claims

DI 1. (Currently Amended) A method in a computer system of executing a first sequence of modules in a first task, said first sequence of modules linked to one another and having at least one sequence of execution, comprising:

- a. storing in each of said first sequence of modules a skip value indicating a next module in said first sequence of modules to execute;
- b. executing a first module of said first sequence of said modules; and
- c. executing said next module of said first sequence of modules indicated by the skip value stored in a currently executed module, skipping any module between the currently executed module and said next module, wherein each module of said first sequence of modules comprises at least one digital signal processing data structure.